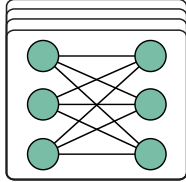




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Deliverable

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1. Publishable summary

This deliverable gives an overview of the software development to support the reference designs used for the demonstrators. An overview of the hardware is given which is used for the demonstrator and what software is used where in the system. It describes the FPGA firmware, Controller firmware, platform APIs.

The described platforms are:

- SENECA a Scalable Energy efficient Neuromorphic Computing Architecture of IMEC-NL
- The API for the SNN architecture platform of IFAG
- GEMM accelerator Xilinx FPGA board set-up of Bosch
- SCALA - Target for hardware deployment and acceleration of VIC, CEA
- Digital accelerator ASIC setup of FhG, VID