

## DG Connect – EDGE IA Workshop (17.01.2020) Hardware and automotive – a tier1 supplier approach

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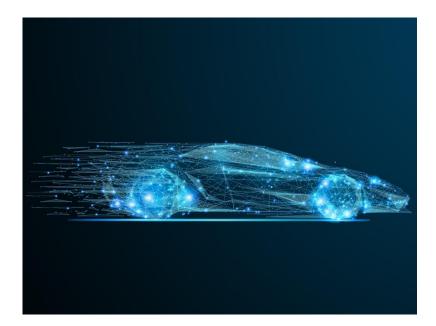


RESEARCH & DEVELOPMENT CENTERS





#### **VALEO'S STRATEGY - BASED ON 2 PRIORITIES**





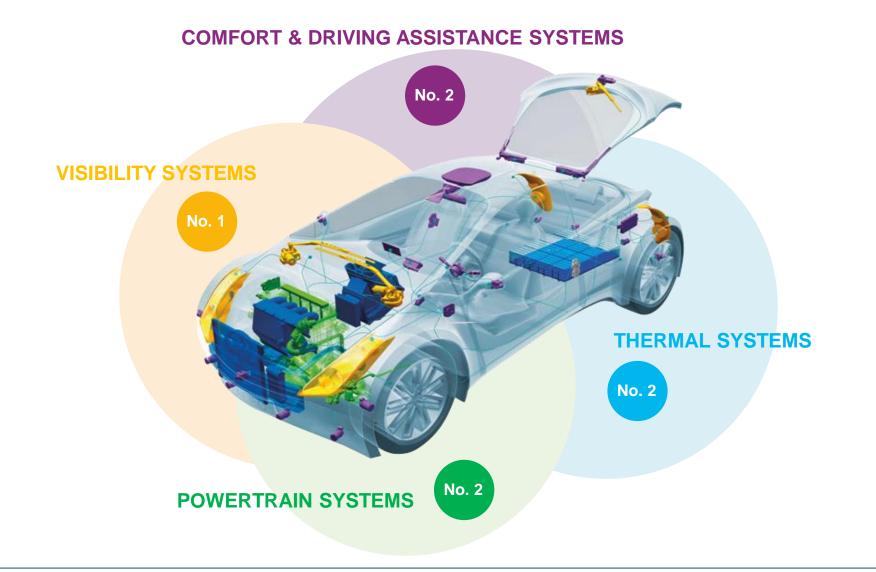
#### INNOVATION

CO<sub>2</sub> emissions reduction and autonomous driving for cleaner, safer, more intuitive vehicles

#### GROWTH

Reinforcement in high growth potential regions







#### **LEADERSHIP POSITION IN AUTOMOTIVE REVOLUTIONS**



2 OUT OF 3 EUROPEAN OEMs HV LAUNCHES IN 2019-2022 EQUIPPED WITH OUR HV SOLUTIONS 1 OUT OF 4 PRODUCED VEHICLES IN 2019 EQUIPPED WITH OUR ADAS SOLUTIONS



#### **LEADERSHIP IN ADAS**





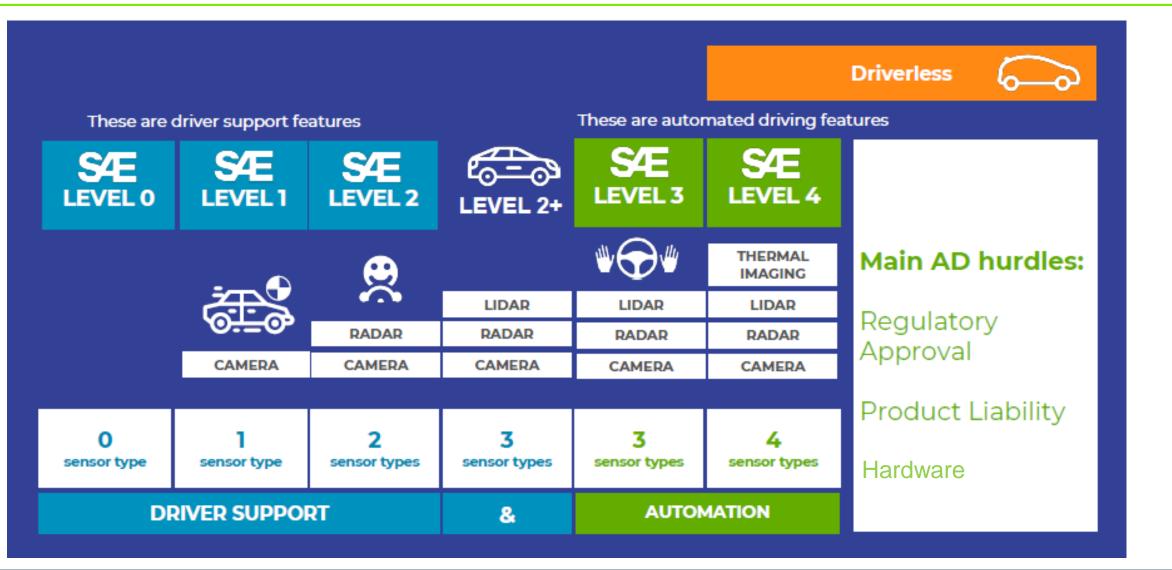
## FROM ADAS TO AD...

## ...MORE IS EXPECTED

## = MORE SAFETY = MORE FUNCTIONS = MORE RELIABILITY & SAFETY EXPECTED



#### SAFER MOBILITY WILL BE DRIVING ADAS & AD GROWTH



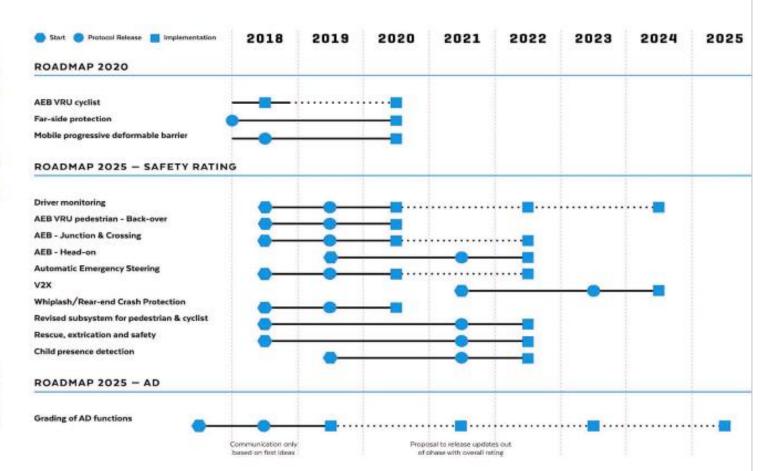
Valeo

#### **ACTIVE SAFETY PUSHED BY REGULATORS**

EU GENERAL SAFETY REGULATION (2019)



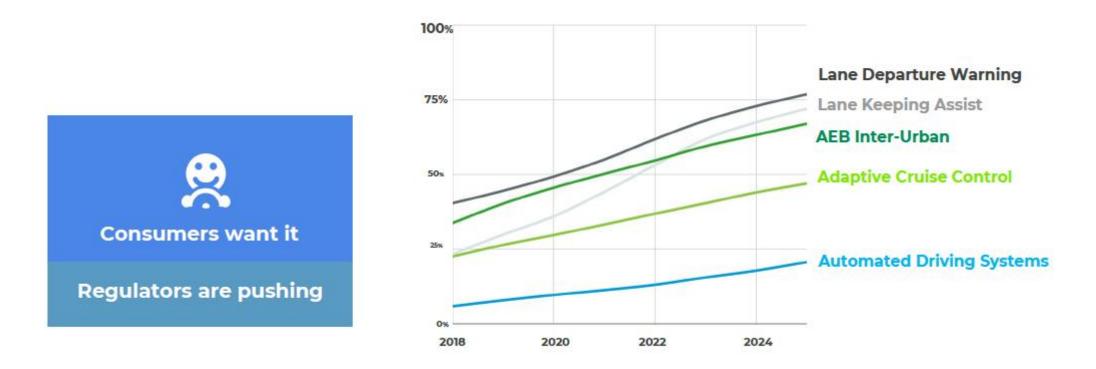
Regulators are pushing



Timeline

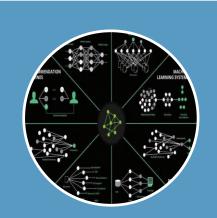


#### TAKE RATE FOR ADAS WILL CONTINUE TO GROWTH



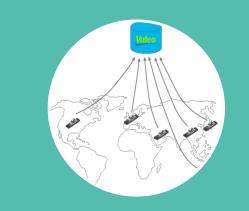


#### A NEED TO MASTER ALL DIMENSION FOR A SYSTEM & FUNCTION SUPPLIER



#### Algorithms

- Design adapted network
- State of the art expertise
- → Valeo.ai creation



Data

- Performances driven by data
- Data mining needed & Annotation cost
- → Data factory



#### Computing power

- Embedded constraints
- Optimization needed
- → Identify hardware platform & design to fit hardware capabilities



#### Validation & Safety

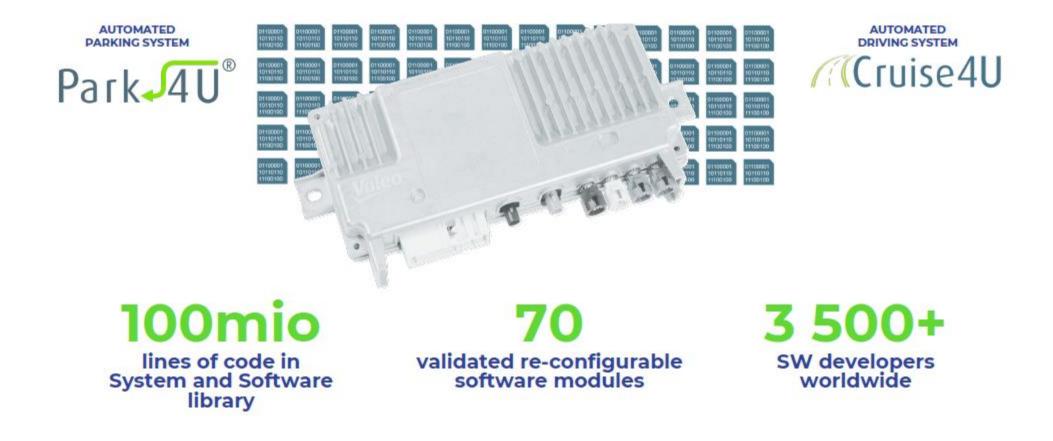
- Explicability
- Validation domain
- → Design redundancy & validation process

Connectivity

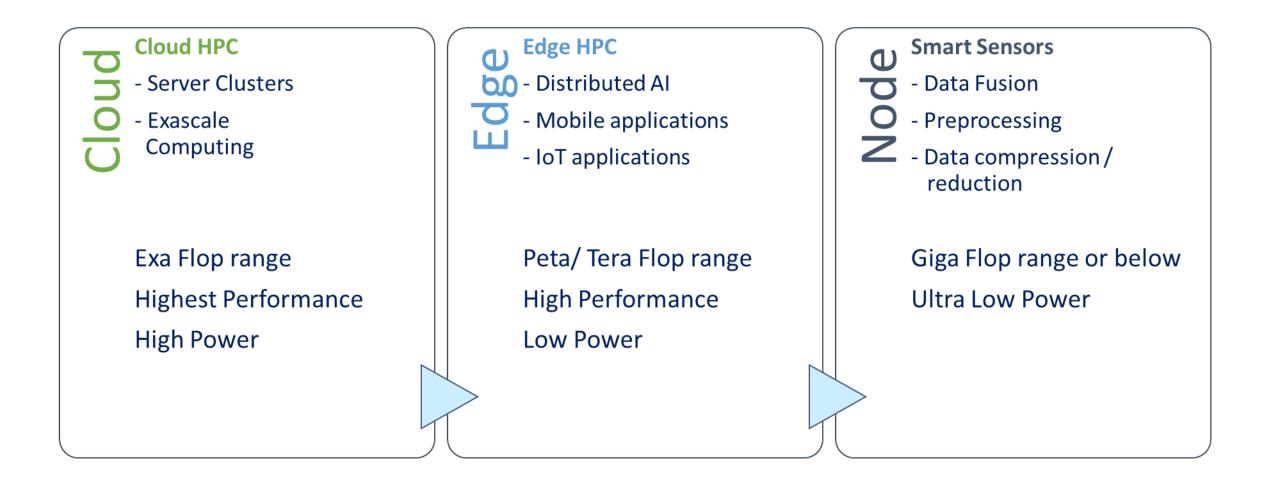




#### IN ORDER TO PROPOSE SCALABLE ADAS & AUTOMATED DRIVING SOFTWARE SYSTEM – FUNCTION FOR A TIER1 SUPPLIER ...

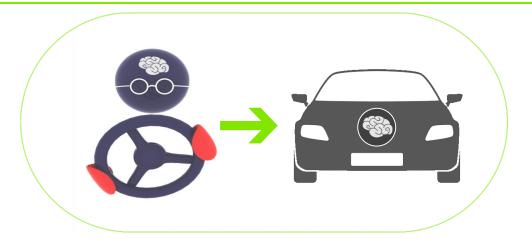


#### # 1 - MASTER HARDWARE DIVERSITY IS A REQUISITE TO MOVE FROM ADAS TO AUTOMATED DRIVING





#### **# 2 - AI IS SAFETY CRITICAL**



#### **Driving Assistance**

- Driver is responsible
- Avoid False Positives Non necessary emergency braking
- The driver handles False Negatives Non detected obstacles

#### Al is comfort (up to now)

#### **Autonomous Car**

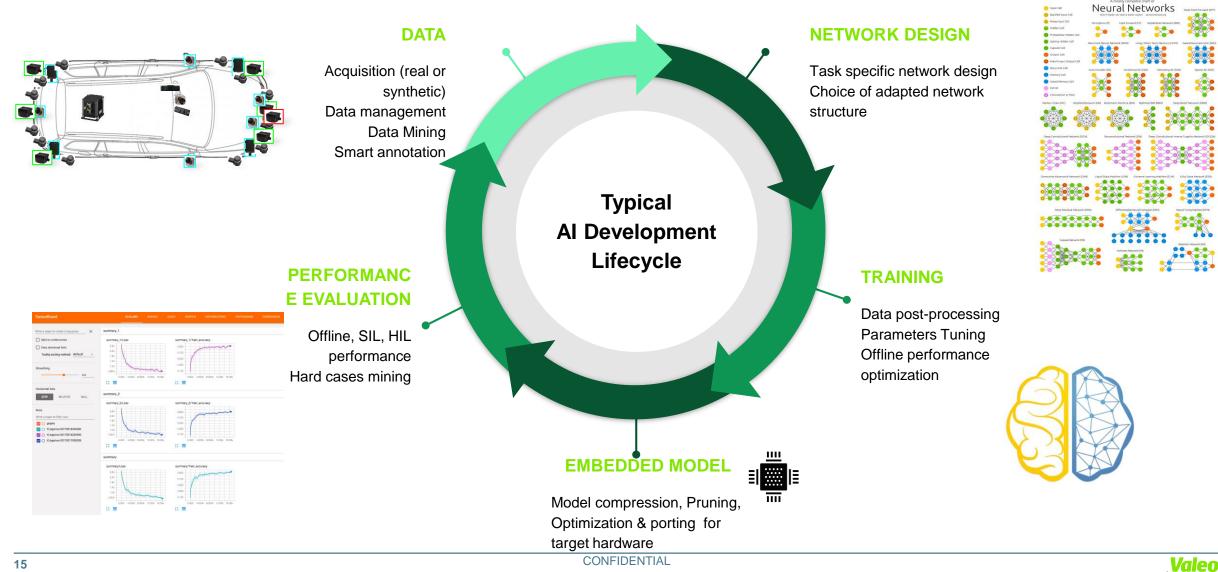
- OEM is responsible
- False Positives are forbidden
- False Negative even more forbidden



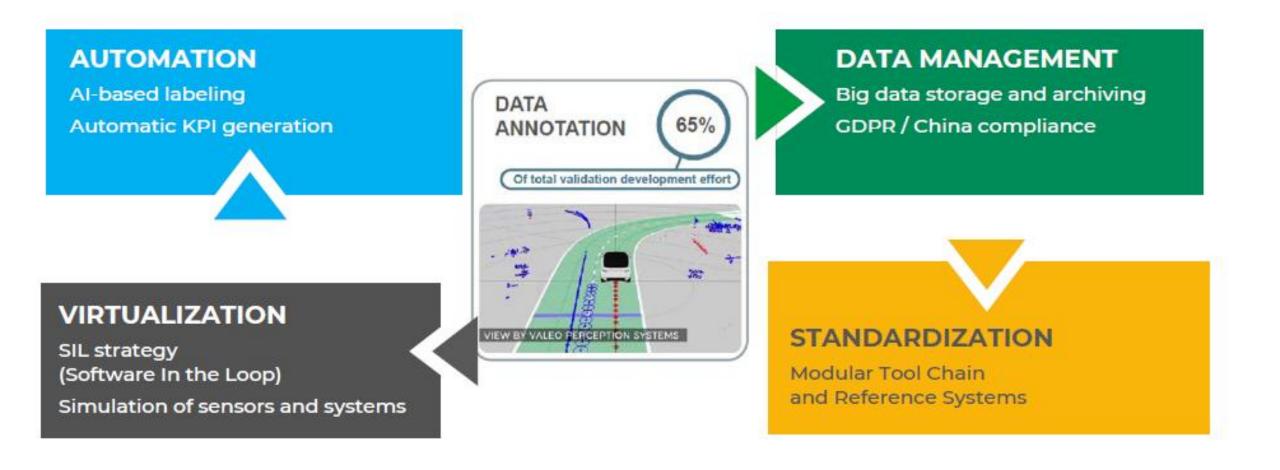




#### A NEED TO MASTER ALL ASPECTS:



#### **# 3 - SYSTEM VALIDATION IS A KEY REQUISITE**





# REINFORCED COLLABORATION IS KEY

## DEFINE, TEST & VALIDATE NEW HARDWARE IN A COLLABORATIVE APPROACH





### **NEW HARDWARE FOR AD: TESTING AT EARLY STAGE**



#### **"TEMPO"** : Neuromorphic HARDWARE nfineon ECSEL 2018<sup>(\*)</sup> Spiking Algo. Deep Neural Far-Out DHILIDS (19 partners, 33 ME budget) Learning Networks Impl. المكم المكم الم **Mixed-Signal** Spike Transient Innovative Design : CNN and SNN Production-Level Maturity Arithmetic CEA Processing BOSCH Analog **Different Memories** imec imecNL MRAM FhG **FeRAM** PCRAM SpiNNaker IIIIIInnoSer Digital **OxRAM** ASIC, VLIW, FPGA Innovative Sensor Technol Contemporary **Digital NoC** GPU, CPU Silicon Technology **TEMPO Scope** ThalesAl 300mm Silicon wafers a Thales / Leonardo company Next EU proposal: "ANDANTE" (ECSEL 2019) 22 and 28nm FDSOI technology

(\*) The TEMPO project has received funding from the Electronic Components and Systems for European Leadership Joint Undertaking under grant agreement No 826655. This Joint Undertaking receives support from the European Union's Horizon 2020 research and innovation programme and Belgium, France, Germany, Switzerland, The Netherlands.



#### **TESTING & VALIDATION OF FUTURE HW & SW TO BE REINFORCED**

FOR SAFETY CRITICAL SYSTEM

FOR RESILIENCE

FOR CUSTOMER ACCEPTANCE

#### EU COMMISSION NEEDS TO SUPPORT TESTING & VALIDATION FOR DEPLOYMENT AND SCALABILITY OF TECHNOLOGY

